

GP-2814
PATENT

Case Docket No. MICRON.003C1

Date: October 13, 1999

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In re application of : Fazan, et al.
App. No. : 09/037,945
Filed : March 10, 1998
For : STREAMLINED FIELD
ISOLATION PROCESS
Examiner : Unknown
Art Unit : 2814

I hereby certify that this correspondence and all marked
attachments are being deposited with the United States Postal
Service as first class mail in an envelope addressed to:
Assistant Commissioner for Patents, Washington, D.C.
20231, on

October 13, 1999

(Date)

James B. Bear, Reg. No. 25,221

**ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231**

Sir:

Transmitted herewith is an amendment in the above-identified application.

(X) An extension of time to respond for 2 month(s) is hereby requested.

Time Extension Fee:

☐ one month (\$110 large entity)
☒ two months (\$380 large entity)
☐ three months (\$870 large entity)

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The fee has been calculated as shown below:

CLAIMS AS FILED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	11	13	= 0 x	\$18	= \$0
Independent Claims	3	4	= x	\$78	= \$0
If application has been amended to contain multiple dependent claim(s), then add				\$260	= \$0
Time Extension Fee					\$380
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$380

10/19/1999 AIBRAH 00000046 09037945

01 FC:116

380.00 OP

0

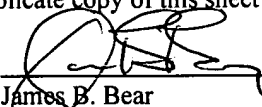
PATENT

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- (X) Return prepaid postcard.
 - (X) A check in the amount of \$380 is enclosed.
 - (X) Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410. A duplicate copy of this sheet is enclosed.



James B. Bear
Registration No. 25,221
Attorney of Record

E #7/B
4 1/59J

Applicant : Fazan, et al.

Appl. No. : 09/037,945 ✓

Filed : March 10, 1998 ✓

For : STREAMLINED FIELD ISOLATION PROCESS

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Assistant Commissioner for Patents
Washington, D.C. 20231

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Please cancel Claim 13, 15 without prejudice.

Please amend the claims as follows:

1. (Twice Amended) A process of forming an integrated circuit, comprising:
growing a silicon dioxide field isolation region on a semiconductor wafer exclusively by means of a hydrogen-free oxidant at a pressure less than about 30 atm; and
forming a gate oxide without a prior sacrificial oxidation.

8. (Twice Amended) A field isolation region among integrated circuit devices on a semiconductor substrate formed by a process comprising:
- exposing a field region of the semiconductor substrate to a [dry] hydrogen-free oxidizing ambient at a pressure between about 5 atm and 30 atm [without any wet oxidation].